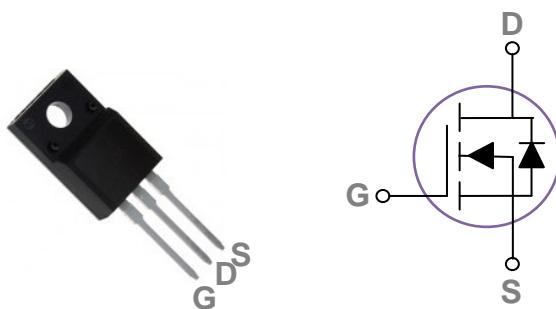


General Description

These N-Channel enhancement mode power field effect transistors are using Super Junction technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply

TO220F Pin Configuration



Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	650	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	11	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	7	A
I_{DM}	Drain Current – Pulsed ¹	44	A
EAS	Single Pulse Avalanche Energy ²	250	mJ
IAS	Single Pulse Avalanche Current ²	1.94	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	31	W
	Power Dissipation – Derate above 25°C	0.25	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	4	$^\circ\text{C}/\text{W}$



650V N-Channel MOSFETs

PJF11N65A

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	650	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.6	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=650\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=520\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	50	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=2.5\text{A}$	---	0.32	0.38	Ω
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 320\mu\text{A}$	2	---	4	V
$\Delta V_{\text{GS(th)}}$	$V_{\text{GS(th)}}$ Temperature Coefficient		---	-6	---	$\text{mV}/^\circ\text{C}$

Dynamic and switching Characteristics

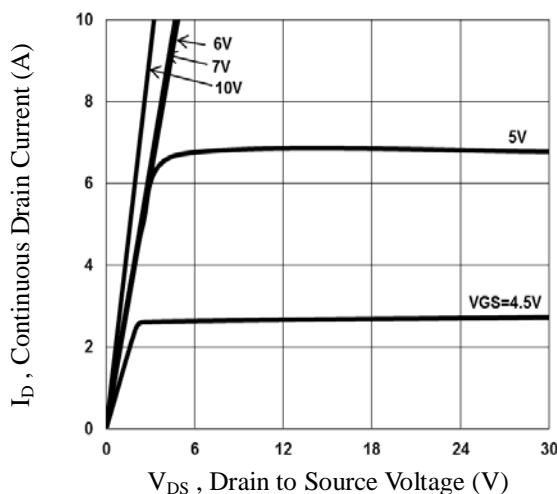
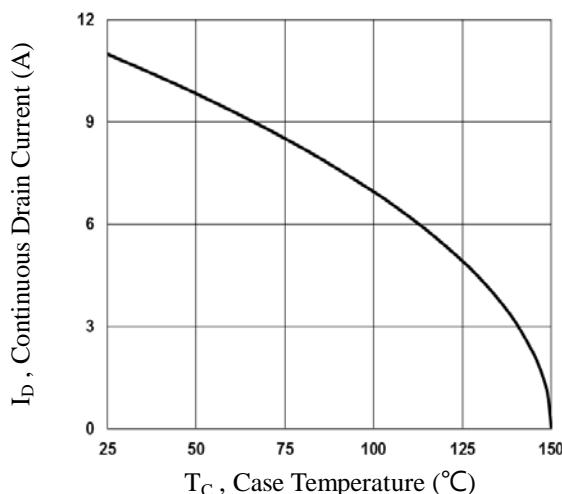
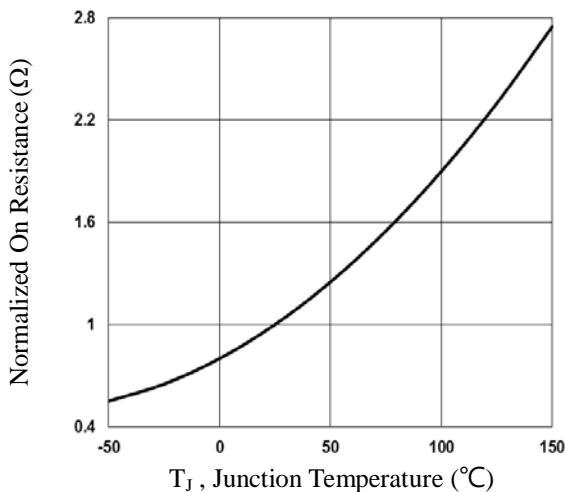
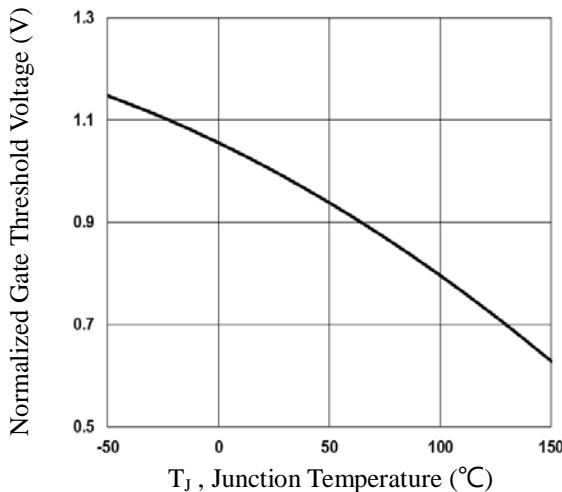
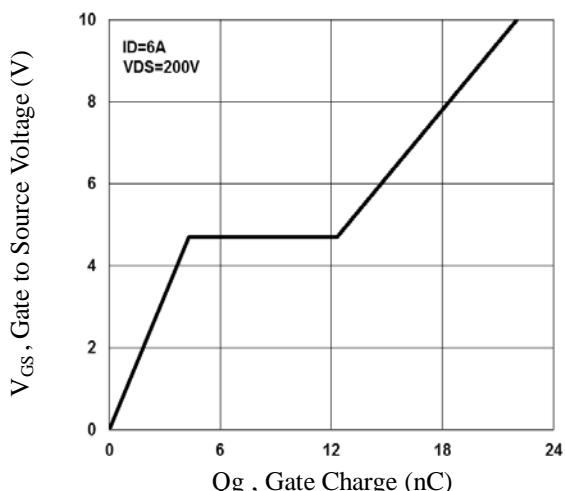
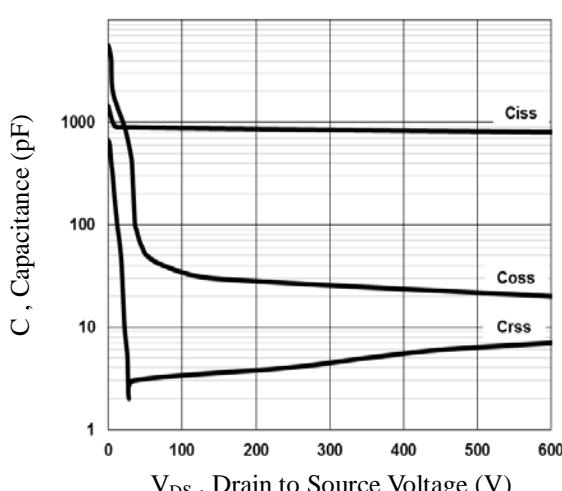
Q_g	Total Gate Charge ^{2,3}	$V_{\text{DS}}=200\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=6\text{A}$	---	22	31	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	4.3	7	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	8	12	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2,3}	$V_{\text{DD}}=400\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=3.4\Omega$ $I_D=-1\text{A}$	---	11	21	ns
T_r	Rise Time ^{2,3}		---	6	11	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2,3}		---	29	55	
T_f	Fall Time ^{2,3}		---	6	11	
C_{iss}	Input Capacitance	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	804	1170	pF
C_{oss}	Output Capacitance		---	34	50	
C_{rss}	Reverse Transfer Capacitance		---	3.4	10	

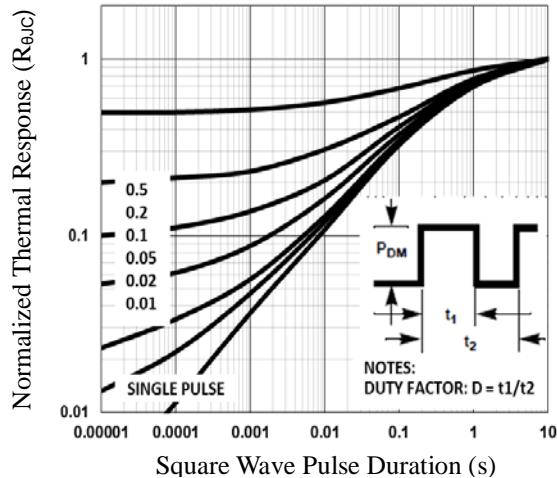
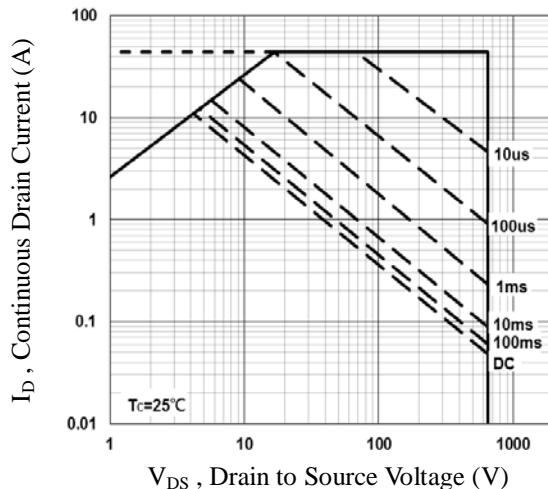
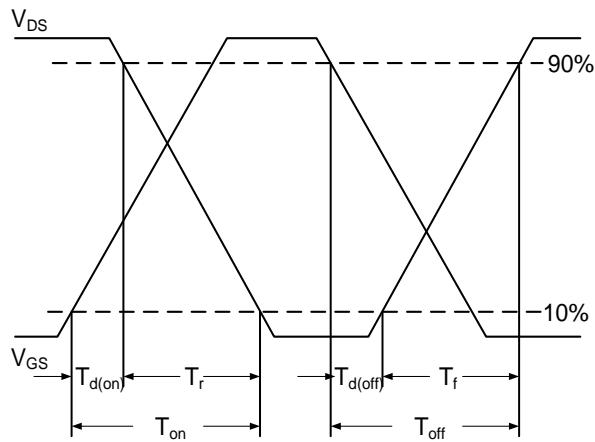
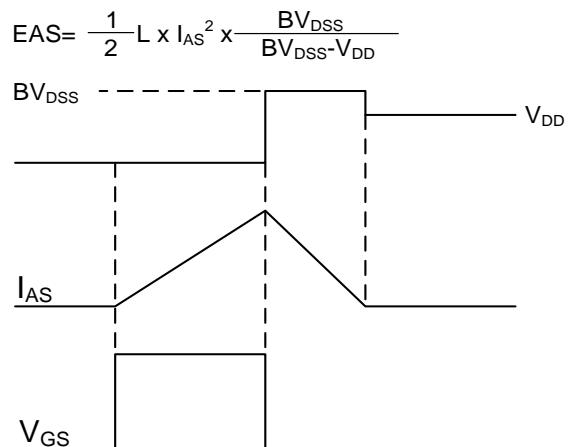
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	11	A
	Pulsed Source Current		---	---	44	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V
t_{rr}	Reverse Recovery Time ²	$V_{\text{GS}}=0\text{V}, I_s=1\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	247	---	ns
	Reverse Recovery Charge ²		---	2.46	---	μC

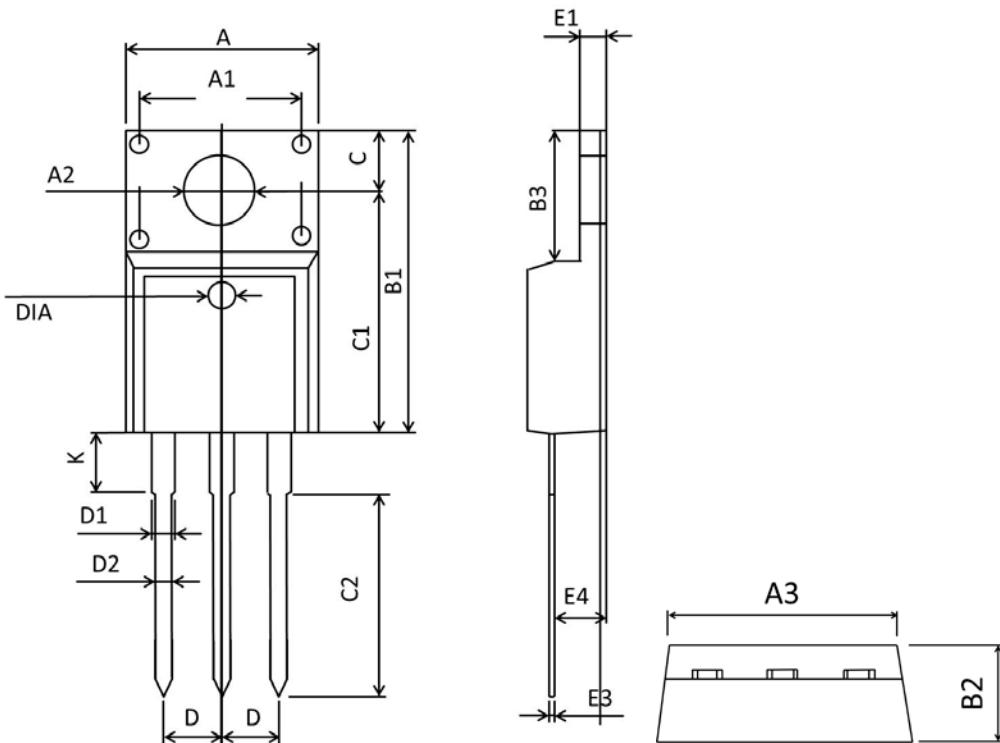
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=133\text{mH}$, $I_{\text{AS}}=1.94\text{A}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.
3. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.


Fig.1 Output Characteristics

Fig.2 Continuous Drain Current vs. T_C

Fig.3 Normalized $R_{DS(on)}$ vs. T_J

Fig.4 Normalized V_{th} vs. T_J

Fig.5 Gate Charge Waveform

Fig.6 Capacitance Characteristics


Fig.7 Normalized Transient Impedance

Fig.8 Maximum Safe Operation Area

Fig.9 Switching Time Waveform

Fig.10 EAS Waveform

TO220F PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	9.960	10.360	0.392	0.408
A1	6.800	7.200	0.268	0.283
A2	3.080	3.280	0.121	0.129
B1	15.780	15.980	0.621	0.629
B2	4.600	4.800	0.181	0.189
B3	6.500	6.800	0.256	0.268
C	3.250	3.400	0.128	0.134
C1	12.530	12.580	0.493	0.495
C2	9.610	10.010	0.378	0.394
D	2.540 BSC		0.100 BSC	
D1	1.120	1.420	0.044	0.056
D2	0.760	0.840	0.030	0.033
K	2.970	3.370	0.117	0.133
E1	2.440	2.640	0.096	0.104
E3	0.400	0.600	0.016	0.023
E4	2.660	2.860	0.105	0.113
DIA	1.400	1.600	0.055	0.063